



Pending

Active

- ☞ L3: (932) memory and sync
- ☞ L4: (853) memory and sync
- ☞ L5: (221) (memory same sy
- ☞ L6: (47) (memory same syn
- ☞ L7: (47) 6 and control
- ☞ L8: (47) 7 and process\$3
- ☞ L9: (7) 8 and record\$3
- ☞ L10: (38) "5796673"

Search List Browse Queue Clear

DBs US-PGPUB; USPAT; EPO; JPO

Default operator: OR

☒ Burels☒ Highlight all the terms in

BRS form ISJR form Image Text HTML

	Type	L #	Hits	Search Text	DBs
1	BRS	L3	932	memory and synchronous and clock and (phase near3 shift) and output and activat\$3 and internal and external and generator and count\$3	US-PGPUB; USPAT; EPO; JPO
2	BRS	L4	853	memory and synchronous and clock and (phase adj shift) and output and activat\$3 and internal and external and generator and count\$3	US-PGPUB; USPAT; EPO; JPO
3	BRS	L5	221	(memory same synchronous same clock) and (phase near3 shift) and output and activat\$3 and internal and external and generator and count\$3	US-PGPUB; USPAT; EPO; JPO
4	BRS	L6	47	(memory same synchronous same clock same (phase near3 shift)) and output and activat\$3 and internal and external and generator and count\$3	US-PGPUB; USPAT; EPO; JPO
5	BRS	L7	47	6 and control	US-PGPUB; USPAT; EPO; JPO
6	BRS	L8	47	7 and process\$3	US-PGPUB; USPAT; EPO; JPO
7	BRS	L9	7	8 and record\$3	US-PGPUB; USPAT; EPO; JPO
8	BRS	L10	38	"5796673"	US-PGPUB; USPAT; EPO; JPO